

RETRY OF A DEVICE READ TRANSACTION

ABSTRACT OF THE DISCLOSURE

An apparatus (11) causes invalid data to be read again from a memory device (12, 13) before
5 being read by a device (10). A transaction queue (22) stores pending and dispatched device
transactions, the queue includes an input for receiving (21) transactions, an output for
dispatching (26) transactions, a pointer for pending transactions, and a pointer for dispatched
transactions. A master controller (42) responds to an invalid data signal by preventing the
10 transaction queue from dispatching pending transactions to the memory device, by causing the
transaction queue to dispatch again the device read transaction which resulted in the invalid data
and, subsequently, by causing the data which was read again from the memory device to be
accepted by the destination device, by setting the dispatched transaction pointer to the pending
transactions pointer, and by enabling the transaction queue to dispatch pending transactions to
the memory device.